

LESSON PLAN

Period	Date	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
1	8/1/19	Review of number systems: Introduction	1	Blackboard		
2	8/1/19	Representation of numbers of different radix	"	"		
3	8/1/19	Conversion of one number from one radix to another radix	"	"		
4	20/1/19	1's Complement	"	"		
5	21/1/19	2's complement of unsigned numbers subtraction	"	"		
6	22/1/19	problem solving	"	"		
7	23/1/19	unsigned binary numbers	"	"		
8	24/1/19	problem solving	"	"		
9	25/1/19	subtraction	"	"		
10	26/1/19	2's complement bit code: BCD	"	"		
11	27/1/19	Excess-3 code	"	"		

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10	28/1/19	2's complement		Blackboard		
		2's complement		"		
		2's complement		"		
11	1/2/19	Logic operation	2	"		
		error detection & correction code: Parity	"	"		
12	1/2/19	Basic logic operations: NOT, OR, AND	"	"		
13	2/2/19	Boolean theorems	"	"		
14	3/2/19	Complement and dual of logical expressions	"	"		
15	3/2/19	NAND & NOR Gates	"	"		
		EX-OR & EX-NOR Gates	"	"		
16	4/2/19	Standard SOP & POS	"	"		
17	5/2/19	Minimization of Logic functions using theorems	"	"		

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18	2/4/19	Generation of self dual function		Method		
		Grover Code		"		
19	10/4/19	Error detection & error correction		"		
		Codes		"		
20	8/4/19	Parity checking		"		
		Even parity add/mul		"		
		Hamming Code		"		
21	10/4/19	Multi-level realization		"		
		Two level NAND - NAND &		"		
		NOR - NOR realization		"		
22	11/4/19	DeMorgan's theorem		"		
		Multi-level realization		"		
23	12/4/19	Minimization of switching function for 2 variables	III	"		
		Using K-Map		"		
24	13/4/19	3 variable K-Map	"	"		
25	14/4/19	4 & 5 variable K-Map	"	"		
		6 variable K-Map	"	"		

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26	15/4/19	Tabular minimization		Blackboard		
27	23/4/19	Minimal SOP & POS realization		"		
28	24/4/19	Problem solving using K-Map		"		
29	25/4/19	Code converters		"		
30	26/4/19	Combinational logic circuit IV		"		
		Design of Half adder		"		
31	27/4/19	Full adder, half subtractor		"		
		Full subtractor		"		
32	28/4/19	Application of RAMs		"		
		4-bit binary adder		"		
33	1/5/19	4-bit binary subtractor		"		
		adder-subtractor		"		
34	2/5/19	BCD adder, Excess-3 adder		"		
35	3/5/19	Look-ahead adder circuit		"		
36	4/5/19	Design of Decoder, V		"		
		Multiplexer, Demultiplexer		"		
37	5/5/19	Higher order Multiplexer & demultiplexer		"		
38	6/5/19	Encoder, Priority encoder		"		

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29	16/11/19	Realization of Combinational Logic		Blackboard		
30	17/11/19	Code Conversion using 2's and 1's		"		
31	18/11/19	SRM	31	"		
32	19/11/19	PLA - PAL		"		
33	20/11/19	Realization of switching function using SRM, PLA & PAL		"		
34	21/11/19	Comparison of PLM, PLA & PAL		"		
35	22/11/19	Programming table of		"		
36	23/11/19	PLM, PLA & PAL		"		
37	24/11/19	Classification of Sequential Circuits	37	"		
38	25/11/19	Basic Flipflops		"		
39	26/11/19	Truth tables & excitation tables		"		
40	27/11/19	Inversion of flipflops		"		
41	28/11/19	Design of		"		
42	29/11/19	Flipflop Circuits		"		
43	30/11/19	Timing Diagrams		"		

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Period	Date	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
53	3/12/19	Ring Counter, Johnson Counter		Blackboard		
54	7/12/19	Design of Registers		"		
55	8/12/19	Buffer Register		"		
56	9/12/19	Control Buffer Register		"		
57	10/12/19	Shift Register		"		
58	11/12/19	Bi-directional shift register & LSR		"		
59	14/12/19	Finite state machine	VIII	"		
60	15/12/19	Capabilities & limitations		"		
61	16/12/19	Analysis of		"		
62	17/12/19	Clocked sequential Circuits		"		
63	21/12/19	Design procedures		"		
64	22/12/19	Reduction of state tables & state assignment		"		
65	23/12/19	Realization of Circuits using various FF's		"		
66	24/12/19	Mealy to Moore Conversion		"		
67	25/12/19	Moore to Mealy Conversion		"		
68	26/12/19	Division		"		
69	31/12/19	Division		"		

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