

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No	Teaching Methodology	Remarks	Corrective Action Upon Review
		<u>Unit - I :</u>				
1	18/3	high pass RC circuit response as a differentiator	I	CR		
2	19/3	Response of high pass RC ckt. for sinusoidal input	I	"		
3	20/3	Response for step input and pulse input	I	"		
4	21/3	Response for square and ramp input	I	"		
5	24/3	RC low pass ckt acts as an integrator	2	"		
6	25/3	Response for step input, pulse input	2	"		
7	26/3	Response for square and ramp input	2	"		
8	27/3	double differentiator attenuator	I	"		
9	28/3	RC ckt response for step input	2	"		
10	1/4	RC ckt response for step input, Raging d.c.	I	"		
		<u>Unit - II :</u>				
11	3/4	Diode clipping	II	CR		
12	4/4	clipping at two independent level	II	"		
13	5/4	Series and shunt wire clipper	II	"		
14	7/4	Transistor clipper	II	"		
15	10/4	Emitter coupled clipper	II	"		
16	11/4	Comparator	II	"		
17	12/4	Negative clipper	II	"		
18	14/4	precision clipper	II	"		

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19	16/4	Biased clippers	<u>II</u>	CR		
20	17/4	Clipping clt Thrm	<u>II</u>	"		
21	18/4	Effect of diode char on clipping voltage	<u>II</u>	"		
		Unit - III				
22	19/4	Diode as a switch	<u>III</u>	CR		
23	20/4	Transistor as a switch	<u>III</u>	"		
24	21/4	Transistor switching times	<u>III</u>	"		
25	22/4	Small signal parameters of transistor and other variables with them	<u>III</u>	"		
26	23/4	Realization of gates using DTL	<u>III</u>	"		
27	24/4	TTL	<u>III</u>	"		
28	25/4	Realization using ECL and CMOS	<u>III</u>	"		
29	26/4	Comparison of logic families	<u>III</u>	"		
		Unit - IV				
30	30/4	principle of operation of bistable multivibrator	<u>IV</u>	CR		
31	1/5	fixed bias & self biased transistorizing	<u>IV</u>	"		
32	2/5	Triggering in Schmitt	<u>IV</u>	"		
33	3/5	Schmitt trigger clt	<u>IV</u>	"		
34	15/5	Applications	<u>IV</u>	"		
35	29/5	design of bistable multivibrator	<u>IV</u>	"		

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Period	Date Tentative	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
		<u>Unit - V</u>				
36	21/5	principle of operation of monostable multivibrator	V	CR		
37	22/5	collector coupled ast	V	"		
38	23/5	emitter coupled mon	V	"		
39	24/5	Triggering in monostable	V	"		
40	25/5	problems related	V	"		
41	26/5	Analysis of astable multivibrator	V	"		
42	27/5	problems related	V	"		
43	30/5	design of astable multivibrator	V	"		
44	2/6	problems related	V	"		
		<u>Unit - VI</u>				
45	3/6	General features of Transistorized	VI	CR		
46	4/6	methods of generating sine wave	VI	"		
47	5/6	multistage RC coupled	VI	"		
48	6/6	Transistor Miller time base generator	VI	"		
49	9/6	Transistor bootstrap time base generator	VI	"		
50	10/6	Current time base generator	VI	"		
51	11/6	problems related	VI	"		

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		<u>Unit - VII:</u>				
S2	12/6	Principle of synchronizer	<u>VII</u>	CE		
		Frequency division in analog ckt		"		
S3	13/6	Analog modulation ckt	<u>VII</u>	"		
S4	14/6	Monostable multivibrator ckt	<u>VII</u>	"		
S5	15/6	Phase delay & phase filter	<u>VII</u>	"		
S6	16/6	Synchronizer of analog ckt with symmetrical signals	<u>VII</u>	"		
S7	18/6	Side wave frequency division with analog ckt	<u>VII</u>	"		
		<u>Unit - VIII:</u>				
S8	20/6	Monostable blocking oscillator	<u>VIII</u>	CR		
S9	21/6	Base tuning &	<u>VIII</u>	"		
C0	22/6	Emitter tuning	<u>VIII</u>	"		
C1	23/6	Analog blocking oscillator	<u>VIII</u>	"		
G2	24/6	Diode controlled &	<u>VIII</u>	"		
		RC controlled				
C3	25/6	Application	<u>VIII</u>	"		
G4	30/6	Base principle of sampling gate	<u>VIII</u>	"		
C5	1/7	Unidirectional sampling gate	<u>VIII</u>	"		
C6	2/7	Bidirectional sampling gate	<u>VIII</u>	"		
C7	3/7	Reduction of pedestal in gate ckt	<u>VIII</u>	"		

[illegible]

4/7

VIN

CR

69

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Application of sampling

$$v \cdot v$$

write complete
date 2 Teaching
methodology
Samy
5/7/19