

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
1	15/03/14	Review of number systems.	I	Black Board		
2	19/03/14	Representation of numbers of different radix.		"		
3	21/03/14	Conversion of num (rns) from one		B.B.		
4	24/03/14	Radix to another Radix.		B.B.		
5	25/03/14	8-1's complement.		B.B.		
6	26/03/14	8's complement subtraction.		B.B.		
7	28/03/14	problems.		B.B.		
8	01/04/14	4 bit codes.		B.B.		
9	01/04/14	BCD Addition & sub.		B.B.		
10	02/04/14	Logic operations.	II	B.B.		
11	03/04/14	NOT, OR, NAND, NOR AND gates.		B.B.		
12	05/04/14	Boolean theorems.		B.B.		
13	07/04/14	Standard SOP and POS		"		
14	09/04/14	Minimization of logic function.		B.B.		
15	05/04/14	Error detection and correction		B.B.		
16	09/04/14	codes. binary codes.		B.B.		
17	11/04/14	NAND - NAND Realization.		"		
18	14/04/14	NOR - NOR Realization.		"		
19	14/04/14	Degenerative - 3 input and multi level		B.B.		
		Realization		B.B.		

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20	15/04/14	Minimization of Switching functions	III	B.B		
21	16/04/14	using K-map up to 6 variables		B.B		
22	17/04/14	"		B.B		
23	21/04/14	Tabular minimization		B.B		
24	22/04/14	Minimal SOP and POS Realisation		B.B		
25	22/04/14	Problems		B.B		
26	23/04/14	"	IV	B.B		
27	25/04/14	Combinational Logic circuit I		B.B		
28	28/04/14	Half Adder		B.B		
29	28/04/14	Half Subtractor		B.B		
30	29/04/14	Full Adder & Full Subtraction		B.B		
31	30/04/14	Application		"		
32	02/05/14	4-bit binary adder 4-bit binary sub	V	"		
33	02/05/14	tractor circuit		B.B		
34	03/05/14	Bcd Adder circuit		B.B		
35	19/05/14	Excess adder circuit		B.B		
36	19/05/14	look-a-head adder circuit		B.B		
37	20/05/14	Combinational logic circuit - II		B.B		
38	21/05/14	Decoder		B.B		
39	22/05/14	Demux		B.B		

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40	24/05/19	Higher Order Demux.		B.B.		
41	24/05/19	Realisation of Boolean functions using.		B.B.		
42	25/05/19	Decoders and Demux.		B.B.		
43	26/05/19	Priority Encoder.		"		
44	27/05/19	Different code converters.		"		
45	30/05/19	Using full adder.		"		
46	02/06/19	Combinational logic circuits - (II)	V	"		
47	02/06/19	PROM.		B.B.		
48	03/06/19	PLA		B.B.		
49	04/06/19	PAL		B.B.		
50	05/06/19	Realisation of switching function		B.B.		
51	06/06/19	using PAL, PROM, PLA.		B.B.		
52	07/06/19	programming tables of PROM, PLA.		B.B.		
53	08/06/19	PROM.		"		
54	09/06/19	Sequential circuit - I	VII	B.B.		
54	11/06/19	Classification of sequential circuits		"		
55	13/06/19	basic flip flops. Truth tables		B.B.		
56	13/06/19	Excitation tables.		B.B.		
57	14/06/19	T, D, RS, JK Flip Flops.		B.B.		
58	15/06/19	"				

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59.	16/06/14	Conversion of flip flops.		B.B		
60.	18/06/14	Design ripple counter		B.B.		
61	20/06/14	Design of Synchronous counters		"		
62	20/06/14	Johnson counters.		"		
63.	21/06/14	Ring counters		B.B		
64	22/06/14	Design of registers		B.B		
65	23/06/14	Buffer registers Control buffer reg.		B.B		
66.	25/06/14	Shift registers.		"		
67.	27/06/14	bidirectional shift reg.		B.B		
68	28/06/14	Universal shift register.		B.B		
69	29/06/14	Sequential circuits. II	VIII	B.B		
		Finite State Machine		"		
70.	30/06/14	Capabilities and limitations.		B.B		
71	01/07/14	clocked sequential circuits.		B.B		
72.	03/07/14	Design procedures		B.B.		
73.	03/07/14	Reduction of state tables.		B.B.		
74.	04/07/14	Realization of circuits using various flip flop.		B.B		
75	05/07/14	Mealy to mome conversions.		B.B		write later 7/7/14
76	08/07/14	Mealy to mome conversions.		B.B.		

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77	10/07/14	Problems		B-B		
78	11/07/14	problems		B-B		
79	12/07/14	Class test. Exam.		B-B		
80	13/07/14	Revision I Unit Revision		B-B		
81	15/07/14	II Unit. Code conversion.		B-B		
82	17/07/14	III Unit		B-B		
83	18/07/14	IV Unit		B-B		
84	20/07/14	V Unit		B-B		
85	22/07/14	VI Unit		B-B		
86	24/07/14	VII Unit		B-B		
87	25/07/14	VIII Unit		B-B		
88	26/07/14	(Class Test) Exam.		B-B		